

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

Attorney Docket No. _____

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the invention entitled: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND EXPOSURE METHOD, the specification of which is attached hereto unless the following box is checked:

☐ The specification was filed on March 27, 2001
and was assigned Serial No. 09/812,947
(if known)
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof, or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows:

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and checked at right:

Prior Foreign Application(s)			Priority Claimed	
(Number)	(Country)	(Month/Day/Year Filed)	Yes	No
2000-085775	JAPAN	03/27/2000	X	

All foreign applications, if any, for any Patent or Inventor's Certificate filed more than 12 months prior to the filing date of this application:

Country	Application No.	Date of Filing (Month/Day/Year)

I hereby claim the benefit under Title 35, United States Code, § 119(e) or § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status: patented, pending, abandoned

I hereby appoint the following attorneys to prosecute this application and/or any international application and to transact all business in the Patent and Trademark Office connected therewith:

Daniel W. Sixbey, (Reg. No. 20,932)	Stuart J. Friedman (Reg. No. 24,312)
Charles M. Leedom, Jr. (Reg. No. 26,477)	Gerald J. Ferguson, Jr. (Reg. No. 23,016)
David S. Safran (Reg. No. 27,997)	Thomas W. Cole (Reg. No. 28,290)
Donald R. Studebaker (Reg. No. 32,815)	Jeffrey L. Costellia (Reg. No. 35,483)
Tim L. Brackett (Reg. No. 36,092)	Eric J. Robinson (Reg. No. 38,285)
Robert M. Schulman (Reg. No. 31,196)	Thomas M. Blasey (Reg. No. 33,475)

Send Correspondence to: Eric J. Robinson
Nixon Peabody LLP
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
Telephone: (703) 790-9110

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

The undersigned hereby authorize any U.S. attorney or agent named herein to accept and follow instructions from Maeda Patent Office as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney or agent and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys or agents named herein will be so notified by the undersigned.

FULL NAME OF SOLE OR FIRST INVENTOR Hiroshi TAKENAKA	INVENTOR'S SIGNATURE <i>Hiroshi Takenaka</i>	DATE June 14, 2001
RESIDENCE (City, State & Country) Kyoto, Japan		CITIZENSHIP Japan
POST OFFICE ADDRESS (Complete Address including City, State & Country) 2-20-4, Kotari, Nagaokakyo-shi, Kyoto 617-0833, Japan		

Attorney Docket No.: 60188-414
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Hiroshi TAKENAKA : Group Art Unit: 2825
Serial No.: 09/812,947 : Examiner: Brandon Bowers
Filed: March 27, 2001 :
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND EXPOSURE
METHOD

**TRANSMITTAL OF REVOCATION OF POWER OF ATTORNEY
AND REQUEST FOR CHANGE OF ADDRESS**

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450



Sir:

Enclosed is a Revocation of Power of Attorney, Appointment and Certification Under 37 CFR 3.373(B) for the above referenced application.

Please change the USPTO records to reflect our Docket Number as noted above.

Also, please address all future correspondence to:

McDermott, Will & Emery
600 13th Street, N.W.
Washington, D.C. 20005-3096

Respectfully submitted,

McDERMOTT, WILL & EMERY

Dated: 5/22/03

By: [Signature]

Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W., Suite 1200
Washington, D.C. 20005-3096
Tel: (202) 756-8000
Fax: (202) 756-8087

Docket No.: 0819-530

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hiroshi TAKENAKA

Serial No.: 09/812,947

Filed: March 27, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND EXPOSURE METHOD

**REVOCATION OF POWER OF ATTORNEY, APPOINTMENT
AND CERTIFICATION UNDER 37 CFR 3.73(B)**

Commissioner for Patents
Washington, DC 20231

Sir:

The undersigned assignee of the above-identified application hereby revokes all previous Powers of Attorney and appoints the following attorneys with full power to prosecute the application, to make alterations and amendments therein, and to transact all business in the United States Patent Office connected therewith.

I hereby appoint the following attorney(s) and/or agent(s): Steven W. Allis, Reg. No. 50,532; Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Willem F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; John A. Hankins, Reg. No. 32,029; Eric J. Kraus, Reg. No. 36,190; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Burman Y. Mathis III, Reg. No. 44,907; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Gene Z. Robinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; Jiri F. Smetana, Reg. No. 52,456; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; Wesley Strickland, Reg. No. 44,363; Michael D. Switzer, Reg. No. 39,552; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti,

Serial No.: 09/812,947

Reg. No. 39,048; Daniel S. Trainor, Reg. No. 43,959; Shival P. Virmani, Reg. No. 45,032; Kelli N. Watson, Reg. No. 47,170; Cameron K. Weiffenbach, Reg. No. 44,488; Aaron Weisstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; Robert W. Zelnick, Reg. No. 36,976; and Wei-Chen Chen, admitted under 37 CFR 10.9(b) all of

McDERMOTT, WILL & EMERY
600 13th Street, N.W.
Washington, DC 20005-3096

with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and all future correspondence should be addressed to them.

CERTIFICATE UNDER 37 CFR 3.73(b)

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., a corporation of 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501, Japan, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of:

An Assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel 012009, Frame 0367.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

The undersigned further declares that all statements made herein of its own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States


Serial No.: 09/812,947

Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Respectfully submitted,

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

Name: Isamu SHIMURA
Title: Director, IP Development Center
Authorized Signing Officer

Signature: 
Date: 04 . 17 . 2003